

GIGABIT IP PHONE CHIP

FEATURES

- The BCM1103 is the world's first IP phone chip that incorporates a Gigabit Ethernet (GbE) switch, hardware security, advanced quality of service (QoS) techniques, and increased processing performance in a single chip, enabling full-featured desktop IP phones.
- The BCM1103 integrates:
 - MIPS32® CPU
 - ZSP DSP with dual-MAC
 - Three-port 10/100/1000BASE-T Ethernet switch
 - Two 10/100/1000BASE-T Ethernet MACs
 - Two 10/100BASE-T Ethernet transceivers
 - Two RGMII ports for use with external Gigabit Ethernet transceivers
 - AES and SHA-1 hardware crypto acceleration
 - Hardware random-number generator
 - Two wideband audio ADCs and DACs with integrated programmable gain amplifiers
 - USB host interface
 - Touchscreen digitizer to support touchscreen displays
 - High-speed UART supporting Bluetooth® UART transport layer protocol
 - General-purpose UART
 - Two IrDA encoders and decoders
 - Keyscan interface
 - LED matrix control
 - LED cadence control
 - General-purpose I/O with programmable direction
 - IEEE 1149.1 (JTAG)
 - DDR SDRAM controller
 - Multifunction Peripheral Interface supporting PCI, PCMCIA, CardBus, EBI devices
- 1.0W peak, 0.15W standby
- 420-pin PBGA Pb-free package

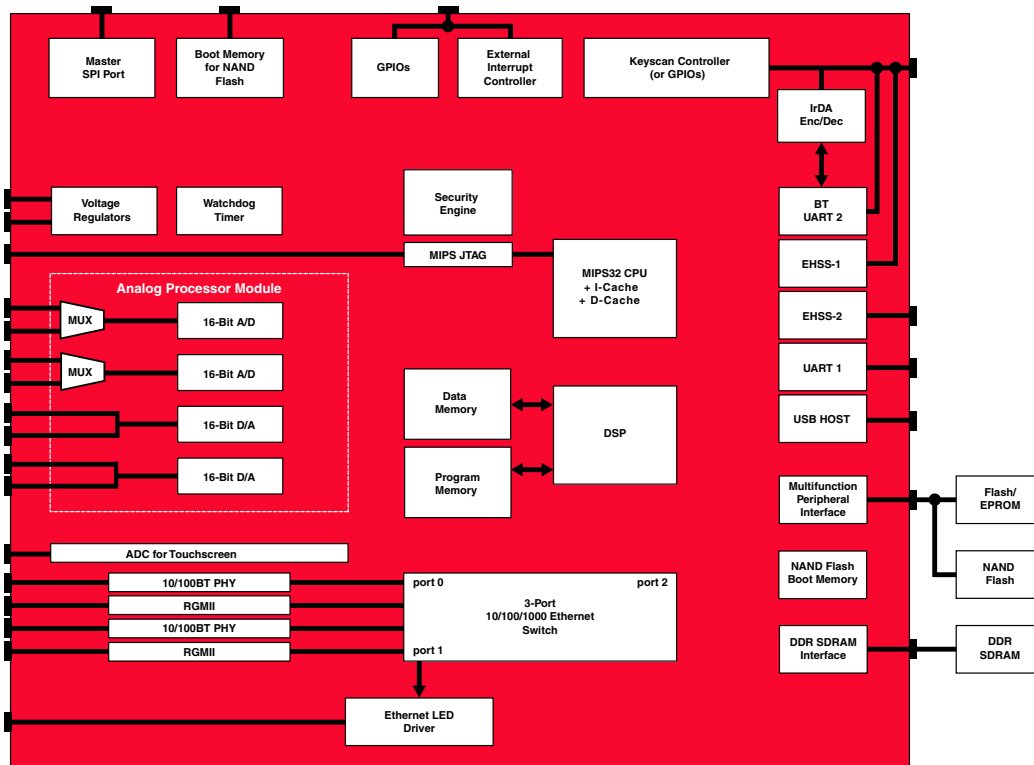
SUMMARY OF BENEFITS

- **10/100/1000BASE-T Ethernet Switch**
 - Configurable switch supports both Fast Ethernet and Gigabit Ethernet operation
 - Supports IEEE 802.1p voice prioritization, 802.1Q VLAN, and 802.1x authentication for advanced Quality of Service
 - Internal switch buffer ensures wire-speed, non-blocking operation
- **Gigabit Ethernet MACs and RGMII Interfaces**
 - Enable Gigabit IP phone products through the use of external Gigabit Ethernet transceivers
- **10/100BASE-T Ethernet MACs and PHYs**
 - Enables cost-effective 10/100 IP phone designs
 - Compliant with IEEE 802.3af power over Ethernet standard
 - Integrates support for Modified Link Pulse in-line power specification
- **RISC and DSP Support**
 - Wind River® platform for consumer devices OS
 - Advanced customer applications (e.g., GUI, web browsing, instant messaging, etc.)
 - Protocol stacks: SIP, H.323, MGCP, Megaco/H.248
 - Narrowband vocoders: G.711, G.729A/AB, G.723.1, G.726, BroadVoice16™, GSM-AMR, GSM-EFR
 - Wideband vocoders: G.722, G.722.1, G.722.2 (GSM-AMR-WB), BroadVoice32™
 - Full-duplex speakerphone with AEC and AGC
 - Handset, headset echo suppression
 - Broadcom and ITU packet-loss concealment algorithms
 - Voice Activity Detection (VAD), Comfort Noise Generation (CNG)
 - Multiparty conferencing with complex vocoders
- **Flexible peripheral interface architecture enables connection to wireless LAN, Bluetooth, and video devices without glue logic**

HARDWARE PLATFORM SUPPORT

- **BCM91103SP Gigabit IP reference design includes:**
 - A housing that features a large graphics display
 - Reduced time-to-market
 - PhonexChange™ suite and example application software

OVERVIEW



BCM1103 Block Diagram

The Broadcom BCM1103 IP phone chip enables manufacturers to build IP phones with integrated Gigabit Ethernet switching, improved voice quality, hardware security, and advanced graphics-driven applications.

The chip integrates a Gigabit Ethernet (10/100/1000 Mbps) switch and two fast Ethernet (10/100 Mbps) transceivers, allowing for the development of traditional fast Ethernet IP phone designs without the additional cost of adding external transceivers. With the addition of external Gigabit Ethernet transceivers, manufacturers can easily upgrade their designs to create Gigabit Ethernet IP phone models.

The chip's high-performance RISC processor and DSP support Broadcom's field-proven PhonexChange IP telephony software suite. PhonexChange includes a wide variety of narrowband and wideband voice coders, including Broadcom's high-fidelity BroadVoice[®] codec, a high-quality full-duplex speakerphone algorithm, advanced jitter buffer management and packet loss concealment techniques, and a complete

library of standard telephony algorithms required for IP phone designs. The RISC processor further allows customers to add product-differentiating application software.

The chip integrates a variety of IP phone peripherals including wideband-capable analog codecs (coder/decoder) with integrated amplifiers, IrDA, USB host, and a touchscreen digitizer. It also provides flexible interfaces for external peripherals and memory types. This high level of integration reduces the phone's bill of material (BOM) costs and enables phone vendors to build scalable IP phone solutions using a single architecture.

Associated devices:

- BCM2035 Bluetooth Single-Chip HCI Solution
- BCM5482 dual 10/100/1000BASE-T Gigabit Copper Transceiver
- BCM4318 WLAN

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